Attorney Docket No.: CYPR-CD00169.CO

1c.

certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class and addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

Name of Person

Signature of the Person Making the Deposit: 06/30/06 Lindsay Kies Deposit: Making the Deposit:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of	: Snyder et al.	Confirmation No.:	
Serial No.:	10/803,030	Art Unit:	
Filed:	03/16/04	Examiner:	Not yet assigned
For:	PROGRAMMABLE MICROCONTROLLER ARCHITECTURE (MIXEI ANALOG/DIGITAL))	
	INFORMATION D	ISCLOSURE STATEMENT	,
Commissioner f P.O. Box 1450 Alexandria, VA			
Sir:			
certain informat patent application	rdance with the duty of disclosur tion which the Examiner may common. It is requested that the Examination of the application.	nsider material to the examinat	tion of the subject U.S.
1.	Enclosures accompanying this I	information Disclosure Stateme	ent are:
1a.	A list of all patents, pub consideration by the office.	olications, applications, or othe	r information submitted for
1b.	☐ A legible copy of:		
	☐ Each foreign patent;		
	Each publication or that por	tion which caused it to be liste	d on the PTO-1449;

or PCT International Search Report.

to be listed on the PTO-1449 including any claims directed to that portion;

all other information or portion which caused it to be listed on the PTO-1449.

For each cited pending U.S. application, the application specification including the claims, and any drawing of the application, or portion of the application which caused it

An English language copy of search report(s) from a counterpart foreign application

	1d.	Explanations of relevancy (ATTACHMENT 1(d), hereto) or English language abstracts of the non-English language publications.
2.	\boxtimes	This Information Disclosure Statement is filed under 37 C.F.R. §1.97(b):
	2a.	Within three months of the filing date of a national application other than a continued prosecution application under §1.53(d);
	2b.	Within three months of the date of entry of the national stage as set forth in §1.491 in an international application;
	2c.	Before the mailing of the first Office action on the merits;
contin	2d. nued exa	Before the mailing of a first Office action after the filing of a request for mination under §1.114.
3.		This Information Disclosure Statement is filed under 37 C.F.R. §1.97(c) after the period specified in 37 C.F.R §1.97(b), but before the mailing date of any of a final action under 37 C.F.R. §1.113, a notice of allowance under 37 C.F.R. §1.311 or an action that otherwise closes prosecution in the application.
		(Check either Item 3a or 3b)
	3a.	☐ The Certification Statement in Item 5 below is applicable. Accordingly, no fee is required.
	3b.	☐ The \$180.00 fee set forth in 37 C.F.R. §1.17(p) in accordance with 37 C.F.R. §1.97(c) is:
		 enclosed to be charged to Wagner, Murabito & Hao Deposit Account No. 23-0085 (order no.).
		(Item 3b to be checked if any reference known for more than 3 months)
4.		This Information Disclosure Statement is filed under 37 C.F.R. §1.97(d) after the period specified in 37 C.F.R. §1.97(c), but on or before the date of payment of the issue fee.
		(Check either Item 4a or 4b)
	4a.	☐ The Certification Statement in Item 5 below is applicable.
	4.b	The \$180.00 fee set forth in 37 C.F.R. §1.17(p) is: enclosed.to be charged to Wagner, Murabito & Hao Deposit Account No. 23-0085 (order no.).
5.		Certification Statement (applicable if Item 3a or Item 4a is checked)
		(Check either Item 5a, 5b or 5c)
	5a.	In accordance with 37 C.F.R. §1.97(e)(1), it is certified that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not

		more than three months prior to the filing of this Information Disclosure Statement.
	5b.	Each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart application, and the communication was not received by any individual designated in 37 C.F.R. §1.56(c) more than thirty days prior to the filing of this information disclosure statement.
	5c.	Pursuant to 37 C.F.R. §1.704(d), each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart application, and the communication was not received by any individual designated in 37 C.F.R. §1.56(c) more than thirty days prior to the filing of this information disclosure statement.
6.		Copies of each cited U.S. patent and each U.S. patent application publication are not enclosed pursuant to the USPTO OG Notice dated 05 August 2003 waiving the requirement under 37 C.F.R. 1.98(a)(2)(i) for U.S. patent applications filed after June 30, 2003.
7.	\boxtimes	This application is a continuation application under 37 C.F.R. §1.53(b) or (d).
		(Check appropriate Items 7a, 7b and/or 7c)
	7a.	A Petition to Withdraw from issue under 37 C.F.R. §1.313(b)(5) is concurrently filed herewith.
	7b.	Copies of publications listed on Form PTO-1449 from prior application Serial No. 09/924,734, filed on 08/07/01, of which this application claims priority under 35 U.S.C. §120, are not being submitted pursuant to 37 C.F.R. §1.98(d).
	7c.	Copies of the publications listed on Form PTO-1449 were not previously cited in prior application Serial No. , filed on , and are provided herewith.
8.		This is a Supplemental Information Disclosure Statement. (Check Item 8a)
	8a.	This Supplemental Information Disclosure Statement under 37 C.F.R. §1.97(f) supplements the Information Disclosure Statement filed on ******. A bona fide attempt was made to comply with 37 C.F.R. §1.98, but inadvertent omissions were made. These omissions have been corrected herein. Accordingly, additional time is requested so that this Supplemental Information Disclosure Statement can be considered as if properly filed on *********.
9.		In accordance with 37 C.F.R. §1.98, a concise explanation of what is presently understood to be the relevance of each non-English language publication is:
		(Check Item 9a, 9b, or 9c)
	9a.	satisfied because all non-English language publications were cited on the enclosed English language copy of the PCT International Search Report or the search report from a counterpart foreign application indicating the degree of relevance found by the foreign office.

	9b.	set forth in the applicat	ion.	
	9c.	enclosed as an attachm	ent hereto.	
10.			orized to charge any additional fee required or comation Disclosure Statement and/or Petition to Account No. 23-0085	
11.		be, material to patentability	the information cited in this Statement is, or is nor a representation that a search has been ma ounterpart application or PCT International Sea.F.R. §§1.97(g) and (h).	de (other than a
Date:	6/	30/2006	Anthony Murabito WAGNER, MURABITO & HAO Two North Mark Street, Third Floor San Jose, CA 95113 (408) 938-9060	35,295 (Reg. No.)



Attorney Docket No.: CYPR-CD00169.CON

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s):

Snyder et al.

Group Art Unit:

2819

Filed:

03/16/04

Examiner:

Not yet assigned

Application No.:

10/803,030

Title:

PROGRAMMABLE MICROCONTROLLER ARCHITECTURE (MIXED ANALOG/DIGITAL)

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	A	6,144,327	11/2000	Distinti et al.			
	В	6,018,559	01/2000	Azegami et al.			
_	С	5,202,687	04/13/93	Distinti	341	158	06/12/91
	D	5,880,598	03/09/99	Duong	326	41	01/10/97
	E	6,304,101	10/16/01	Nishihara	326	41	06/02/00
	F	5,426,378	06/20/95	Ong	362	39	04/20/94
	G	5,828,693	10/27/98	Mays et al.	375	202	03/21/96
	Н	5,703,871	12/30/97	Pope et al.	370	248	05/26/95
	1	6,018,559	01/25/00	Azegami et al.	377	79	12/16/96

Other Documents

Examiner		·		
Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication		
	J	CYPR-CD00170; "DIGITAL CONFIGURABLE MACRO ARCHITECTURE"; 07/18/01; 09/909,0485; W. Snyder		
	K	CYPR-CD00172; "CONFIGURING DIGITAL FUNCTIONS IN A DIGITAL CONFIGURABLE MACRO ARCHITECTURE"; 07/18/01; 09/909,109; W. Snyder		
	L	CYPR-CD00173; "A PROGRAMMABLE ANALOG SYSTEM ARCHITECTURE (AS AMENDED)"; 07/18/01; 09/909,047; M. Mar		
	М	CYPR-CD00232; "PROGRAMMABLE SYSTEM ON A CHIP"; 10/01/01; 10/033,027; W. Snyder		
Examiner		Date Considered		

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.